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FORM PTO-1449	SERIAL NO. 1055018291 Not yet assigned 10808/195
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE Herewith GROUP ART UNIT Not yet assigned
(use several sheets if necessary)	APPLICANT(S): Ottmar Koeder et al.

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (If known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
TD	A1	US 2001/0047507 A1	November 29, 2001	Pileggi et al.		
TD	A2	6,363,519 B1	March 26, 2002	Levi et al.		
TD	A3	5,914,887	June 22, 1999	Scepanovic et al.		
TD	A4	6,052,677	April 18, 2000	Masuoka et al.		
TD	A5	6,360,191 B1	March 19, 2002	Koza et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (If known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
TD	A6	EP 0431 532 A2	June 12, 1991	EPO		

EXAMINER INITIAL	OTHER ART - NON PATENT LITERATURE DOCUMENTS <small>(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)</small>	
NONE	A7	Copy of International Search Report from International Application No. PCT/DE03/01863.
TD	A8	Mehmet Aktuna, Rob A. Rutenbar and L. Richard Carley, "Device-Level Early Floorplanning Algorithms for RF Circuits" pgs. 375-388, IEEE 1999.
TD	A9	T. Arslan, D.H. Horrack and E. Ozdemir, "Structural Cell-Based VLSI Circuit Design Using a Genetic Algorithm" pgs.308-311, IEEE 1996.
TD	A10	N. Zhuang, M.S.T. Benten and P.Y.K. Cheung, "Improved Variable Ordering of BDDS With Novel Genetic Algorithm" pgs. 414-417, IEEE 1996.
TD	A11	Akio Sakamoto, Xingzhao Liu and Takashi Shimamoto, "A Modified Genetic Channel Router" pgs. 2076-2083, IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences E77-A, No. 2, December 1994.
TD	A12	Imtiaz Ahmad, Muhammad Khan Dhodhi and Frank H. Hielscher, "Design-Spaced Exploration for High-Level Synthesis" pgs. 491-496, IEEE 1994.

EXAMINER <i>Muhammad</i>	DATE CONSIDERED 6-01-2007
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449	SERIAL NO. Not yet assigned	CASE NO. 10/518291 10808-195
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE Herewith	GROUP ART UNIT Not yet assigned
(use several sheets if necessary)	APPLICANT(S): Ottmar Koeder et al.	

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TD	A13	Ernest S. Kuh and Tatsuo Ohtsuki, "Recent Advances in VLSI Layout" pgs. 237-263, Vol. 78, No. 2, IEEE 1990.
TD	A14	David J. Powell, Siu Shing Tong and Michael M. Skolnick, "Domain Independent, Machine Learning for Design Optimization" pgs. 151-159, Proceeding of the Third International Conference on Genetic Algorithms, June 1989.
TD	A15	Daniel Brand, "Hill Climbing with Reduced Search Space" pgs. 294-297, IEEE 1988.
TD	A16	Mario P. Vecchi and Scott Kirkpatrick, "Global Wiring by Simulated Annealing" pgs. 215-222, Vol. CAD-2, No. 4, IEEE October 1983.

EXAMINER <i>Murando</i>	DATE CONSIDERED 6-01-2007
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FORM PTO-1449	SERIAL NO. 10/518,291	CASE NO. 10808/195
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE December 16, 2004	GROUP ART UNIT 2825
(use several sheets if necessary)		APPLICANT(S): Ottmar Koeder et al.

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

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(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.

TD	B1	Corey Kosak, Joe Marks and Stuart Shieber, <i>A Parallel Genetic Algorithm for Network-Diagram Layout</i> , pgs. 458-465, Proceedings of the Fourth International Conference on Genetic Algorithms, July 13-16, 1991.

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